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PRIMARY CHARACTERISTICS

I_{F(AV)}

V_{RRM}

IFSM

V_F at I_F = 4.0 A (T_A = 125 °C)

T_J max.

Package

Diode variation

Vishay General Semiconductor

Surface Mount Trench MOS Barrier Schottky Rectifier



4.0 A

50 V

80 A

0.46 V

150 °C

DO-221BC (SMPA)

Single die

FEATURES

- Very low profile typical height of 0.95 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: DO-221BC (SMPA) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

M3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V4PAN50	UNIT		
Device marking code		4N5			
Maximum repetitive peak reverse voltage	V _{RRM}	50	V		
Maximum DC forward current	I _F ⁽¹⁾	4.0	Α		
	I _F ⁽²⁾	3.0			
Maximum DC reverse voltage	V _{DC}	35	V		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	80	А		
Operating junction and storage temperature range	T _J , T _{STG}	- 40 to + 150	°C		

Notes

⁽¹⁾ Units mounted on 15 mm x 15 mm pad areas, 2 oz. PCB

⁽²⁾ Free air, mounted on recommended copper pad area



COMPLIANT

HALOGEN

FREE

V4PAN50-M3



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 2.0 A	T _A = 25 °C	V _F ⁽¹⁾	0.43	-	v
	$I_F = 4.0 \text{ A}$			0.51	0.59	
	I _F = 2.0 A	T _A = 125 °C		0.34	-	
	I _F = 4.0 A			0.46	0.54	
Reverse current	V _R = 35 V	T _A = 25 °C	I _R ⁽²⁾	8	-	μA
	v _R = 33 v	T _A = 125 °C		8.8	-	mA
	V _B = 50 V	T _A = 25 °C		-	600	μA
	$v_{\rm R} = 50$ V	T _A = 125 °C		12	35	mA
Typical junction capacitance	4.0 V, 1 MH	4.0 V, 1 MHz		480	-	pF

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}$ Pulse test: Pulse width $\leq 5\mbox{ ms}$

THERMAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise specified)				
PARAMETER	SYMBOL	V4PAN50	UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾	100	°C/W	
	R _{0JM} ⁽¹⁾	9	0/10	

Note

⁽¹⁾ Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance R_{0JA} - junction to ambient; R_{0JM} - junction to mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V4PAN50-M3/I	0.032	I	14 000	13" diameter plastic tape and reel		

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

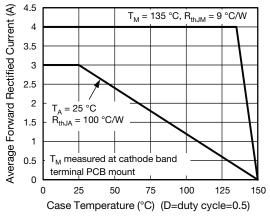


Fig. 1 - Maximum Forward Current Derating Curve

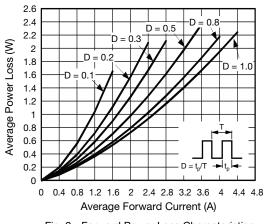
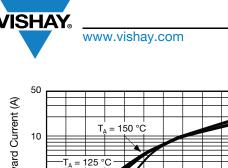


Fig. 2 - Forward Power Loss Characteristics

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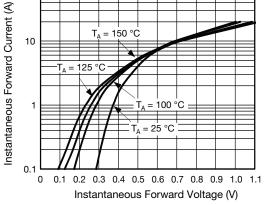


Fig. 3 - Typical Instantaneous Forward Characteristics

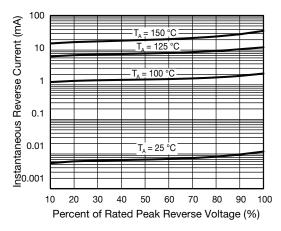


Fig. 4 - Typcial Reverse Leakage Characteristics

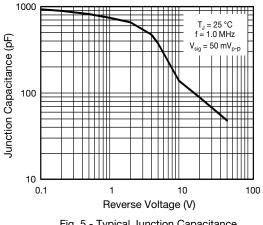


Fig. 5 - Typical Junction Capacitance

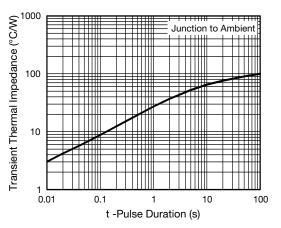


Fig. 6 - Typcial Transient Thermal Impedance

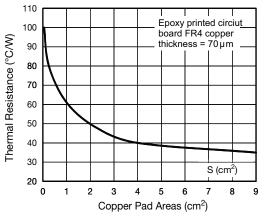


Fig. 7 - Thermal Resistance Junction to Ambient vs. **Copper Pad Areas**

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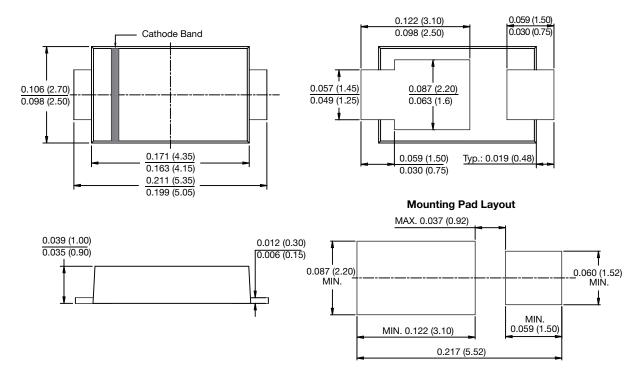
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

DO-221BC (SMPA)





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