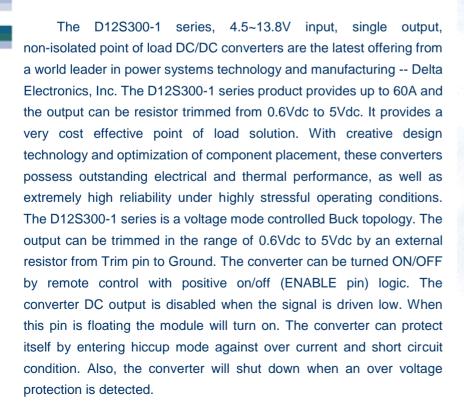




# Delphi D12S300-1 D/E Non-Isolated Point of Load DC/DC Modules: 4.5V~13.8Vin, 0.6V~5Vout, 60A



#### **FEATURES**

- High Efficiency:94% @ 12Vin, 5V/60A out
- Wide input range: 4.5V~13.8V
- Output voltage programmable from
   0.6Vdc to 5Vdc via external resistors
- No minimum load required
- Fixed frequency operation
- Input UVLO, output OCP, OVP.
- Remote On/Off (Positive logic)
- Power Good Function
- RoHs completed
- ISO 9001, TL 9000, ISO 14001, QS9000,
   OHSAS18001 certified manufacturing
   facility

#### **APPLICATIONS**

- Telecom/DataCom
- Distributed power architectures
- Servers and workstations
- LAN/WAN applications
- Data processing applications

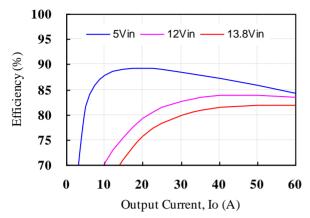


# **TECHNICAL SPECIFICATIONS**

(Ambient Temperature=25°C, minimum airflow=100LFM, nominal V<sub>in</sub>=12Vdc unless otherwise specified.)

	PARAMETER	NOTES and CONDITIONS		D12S300-1			
Input Voltage (Page) Treached   Vision   Visio						Units	
Peter to Fig. 32 for the measuring point					10.0		
Storage Emperature	Operating Temperature					Vdc °C	
13.8	Storage Temperature	Relef to Fig. 52 for the measuring point				°C	
Imput Unice-Vallage Enclosed							
Turn-Off Voltage Protection					13.8	Vdc	
Turn-Orl Voltage Threshold				4.00			
Deckord Hystericals Voltage   Vin=12V, Vout-5V, 10-60A   28   Vin-12V, Vout-5V, 10-60A   28   Vin-12V, Vout-5V, 10-60A   50   600   600   700						Vdc Vdc	
Moorman Input Current   Vin=12V, Voul-6V, Io-60A   S30   600		Without adjust resistor (Reff)				Vuc	
Official Content   Remote OFF Vimit 2V   120 MHz   30   10   10   10   10   10   10   10		Vin=12V, Vout=5V, Io=60A		0.1	28	A	
Input Reflected Ripple Current   PP-P thru 2uth Inductor SPLE to 20MHz   50   50   50   50   50   50   50   5				530	600	mA	
Input Voltage Ripple Rejection					30	mA	
Output DV CHARACTERISTICS         Vin-12V, Yout-6V         160           Output Voltage Adjustment Range         0.6         5.0           Output Voltage Regulation         0.6         5.0           Over Load         Ioulou Voltage Regulation         0.5         0.1         +0.5           Over Load         Ioulou Provided Regulation         0.0         0.0         -0.5         0.1         +0.5           Over Load         Over Load         Ioulou Provided Regulation         0.0         -0.0         0.0         +0.5         0.1         +0.5           Over Load         Over Load         Ioulou Transpear Control of Mark Regulation         0.0         -0.0         -0.0         +0.5         0.1         +0.5         -0.1         +0.5         0.0         +0.5         0.1         +0.5         -0.2         +0.2						mA	
QUTPUT CHARACTERISTICS           Output Vottage Set Point         With a 0.1% trim resistor, measured at remote sense pin.         -0.8         5.0           Output Vottage Set Point         With a 0.1% trim resistor, measured at remote sense pin.         -0.5         0.1         +0.5           Over Load         Ibe-lo, min to 10, max, measured at remote sense pin.         -0.5         0.1         +0.5           Over Load         Yea-Yin, min to Mr. max, measured at remote sense pin.         -0.2         +0.2           Total output range         Sense pin.         -0.2         +0.2           Output Votter Board Replea and Noise         Set 200 Pin Load Apple To coramic, total input & output range         20         50           Peak-o-Peak         Full Load, 200 Fin a cap&fulf ceramic, total input & output range         8         15           Output Urrent Range         Vis-12V, Turn OFF         10         10           Output Urrent Range         Vis-12V, Turn OFF         10         10           Output Urrent Range         Vis-12V, Turn OFF         10         10           Output Supramic Load Response         12V, Turn OFF         10         10           Output Supramic Load Response         11V, Turn OFF         120         10           Transient Response         Output Supramic Load Response						dB mA	
Quipt Vibriage Adjustment Range		VIII-12V, VOUL-3V		100		III/A	
Output Voltage Regluation         With a 0.1% tim resistor, measured at remote sense pin.         -0.8         0.1         +0.8           Over Load         Output Voltage Regluation         Io-Io, min to lo, max, measured at remote sense pin.         -0.5         0.1         +0.5           Over Load         Vin-Win, min to lo, max, measured at remote sense pin.         -0.2         +0.2           Total output range         Over load, line, temperature regulation and set point, measured at remote sense pin.         -1.5         +1.5           Output Voltage Ripple and Noise         SF4z to 20MBb bandwidth         -1.5         +1.5         +1.5           Clubs Wildinge Ripple and Noise         SF4z to 20MBb bandwidth         -1.5         +1			0.6		5.0	V	
Over Load		With a 0.1% trim resistor, measured at remote sense pin.		0.1		%Vo	
Over Line							
Over load, line, temperature regulation and set point, measured at remote sense pin.				0.1		%Vo	
Clast Output Tangle   Sense pin.   1.5   41.5			oto			%Vo	
Output Voltage Ripple and Noise         5Hz to 20MHz bandwidth         County Current Range         50         50           Cutput Current Range         Full Load, 10uF Tan cap& fuF ceramic, total input & output range         8         15           Cutput Voltage Under-shoot at Power-Off         Vin=12V, Turn OFF         0         60           Cutput Voltage Under-shoot at Power-Off         Vin=12V, Turn OFF         0         60           Cutput Voltage Under-shoot at Power-Off         Vin=12V, Turn OFF         0         60           Output Voltage Under-shoot at Power-Off         Vin=12V, Turn OFF         0         100           Output Horizon Current Limit Inception         Hiccup mode         110         180           DVRAMIC CHARACTERISTICS         110         180         120         125         130           DVRAMIC CHARACTERISTICS         120         170         110         160         110         160           Transient Response         Output step load-25% food for all range Slew rate=10Ajus         0.5 Vo         110         160           Transient Response         Output step load-25% food for all range Slew rate=10Ajus         0.9 Vo         120         170           Transient Response         Output step load-25% food for all range Slew rate=10Ajus         1.9 Vo         1.0         160     <	Total output range		-1.5		+1.5	%Vo	
RNS		5Hz to 20MHz bandwidth					
Output Voltage Under-shoot at Power-Off	Peak-to-Peak	Full Load, 20uF Tan cap&1uF ceramic, total input & output range				mV	
Output violate Under-shoot at Power-Off         Vine12V, Turn OFF         100           Output short-circuit current, RNS value         12Vin, SYout         10           Output DC Current-Limit Inception         Hiccup mode         120         125         130           DYNAMIC CHARACTERISTICS         Hiccup mode         120         125         130           Output Dynamic Load Response         12Vin, Juf Certamic, 10uF Tan cap         110         160           Transient Response         Output step load=25% load for all range Siew rate=10A/µs         0.8 Vo         110         160           Transient Response         Output step load=25% load for all range Siew rate=10A/µs         1.2 Vo         120         170           Transient Response         Output step load=25% load for all range Siew rate=10A/µs         1.5 Vo         120         170           Transient Response         Output step load=25% load for all range Siew rate=10A/µs         1.5 Vo         120         170           Transient Response         Output step load=25% load for all range Siew rate=10A/µs         1.5 Vo         120         170           Transient Response         Output step load=25% load for all range Siew rate=10A/µs         5.0 Vo         100         150           Transient Response         Output step load=25% load for all range Siew rate=10A/µs         5.0 Vo		Full Load, 10uF Tan cap&1uF ceramic, total input & output range	_	8		mV	
Output Short-circuit current, RMS value         12Vin, SVout         10           Over Voltage Protection         Hiccup mode         120         125         130           DYNAMIC GHARACTERISTICS         120         120         125         130           DYNAMIC GHARACTERISTICS         120         120         125         130           DYNAMIC GHARACTERISTICS         120         120         170         150         170           Transient Response         Output step load-25% load for all range Slew rate=10A/µs         0.9 %         120         170           Transient Response         Output step load-25% load for all range Slew rate=10A/µs         1.2 %         120         170           Transient Response         Output step load-25% load for all range Slew rate=10A/µs         1.2 %         120         170           Transient Response         Output step load-25% load for all range Slew rate=10A/µs         1.8 %         120         170           Transient Response         Output step load-25% load for all range Slew rate=10A/µs         2.5 %         100         150           Transient Response         Output step load-25% load for all range Slew rate=10A/µs         5.0 %         100         150           Transient Response         Output step load-25% load for all range Slew rate=10A/µs         5.0 %		Vin-12V Turn OFF	0			A mV	
Dupt US Current-Limit Inception				10	100	A	
DYNAMIC CHARACTERISTICS			110	10	180	%	
Output Dynamic Load Response   12Vin, 1uF ceramic, 10uF Tan cap	Over Voltage Protection	Hiccup mode	120	125	130	%	
Transient Response							
Transient Response							
Transient Response						mV pk	
Transient Response						mV pk mV pk	
Transient Response						mV pk	
Transient Response   Output step load=25% load for all range Slew rate=10A/μs   3.3 Vo   100   150	Transient Response			100		mV pk	
Transient Response   Output step load=25% load for all range Slew rate=10A/μs   5.0 vo   100   150   50   50   50   50   50						mV pk	
Settling Time         20         60           Turn-On Transient           Rise Time         From 10% to 90% of Vo         1         2           Turn-on Delay (Power)         Vin=12V, lo=min-max. (within 10% of Vo)         4         10           Turn-on Delay (Remote on/off) )         Vin=12V, lo=min-max. (within 10% of Vo)         0.4         2           Turn-on Eday (Remote on/off) )         Vin=12V, lo=min-max. (within 10% of Vo)         0.4         2           Turn-on Delay (Remote on/off) )         Vin=12V, lo=min-max. (within 10% of Vo)         0.4         2           Turn-on Delay (Remote on/off) )         Vin=12V, lo=min-max. (within 10% of Vo)         0.4         2           Turn-on Delay (Remote on/off) )         Vin=12V, lo=min-max. (within 10% of Vo)         0.4         2           Turn-on Delay (Remote on/off) )         Vin=12V, lo=60A         0         5000           Minimum Output Capacitance         ESR > ImΩ         0         5000           EFFICIENCY         Vin=12V, lo=60A         81         83         83           Vo=1.5V         Vin=12V, lo=60A         88         89.0         88         90.0         90.0         92.1         1         90         92.1         1         90         92.1         1         90         92.1						mV pk	
Turn-on Delay (Power)		Output step load=25% load for all range Slew rate=10A/µs 5.0	/o			mV pk	
Rise Time				20	60	μs	
Turn-on Delay (Remote on/off)         Vin=12V, Io=min-max. (within 10% of Vo)         0.4         2           Turn on & turn off Transient (overshoot)         0.5%         0.5%           Minimum Output Capacitance         ESR≥ 1mΩ         0         5000           EFFICIENCY           Vo=0.6V         Vin=12V, Io=60A         76         78         0           Vo=0.9V         Vin=12V, Io=60A         81         83         0           Vo=1.2V         Vin=12V, Io=60A         84         86.5         0           Vo=1.5V         Vin=12V, Io=60A         86         88.5         0           Vo=2.5V         Vin=12V, Io=60A         90         92.1         92.1           Vo=3.3V         Vin=12V, Io=60A         91         93.4         93.4           Vo=5.0V         Vin=12V, Io=60A         91         93.4         94.5           SINK EFFICIENCY         Vin=12V, Io=60A         93         95           Vo=5.0V         Vin=12V, Io=60A         93         95           Switching Frequency         Fixed, Per phanse         500         93           Switching Frequency         Fixed, Per phanse         500         90           ON/OFF Control         Positive logic (internally pulled high)		From 10% to 90% of Vo		1	2	ms	
Turn on & turn off Transient (overshoot)         0.5%           Minimum Output Capacitance         ESR ≥ 1mΩ         0         5000           EFFICIENCY           Vo=0.6V         Vin=12V, lo=60A         76         78           Vo=0.9V         Vin=12V, lo=60A         81         83           Vo=1.5V         Vin=12V, lo=60A         86         88.5         0           Vo=1.8V         Vin=12V, lo=60A         88         90.0         0	Turn-on Delay (Power)			4		ms	
Minimum Output Capacitance   ESR ≥ 1mΩ   5000   EFICIENCY   5000   5					2	ms	
FFICIENCY   Vin=12V, lo=60A   76   78   78   78   78   79   79   79   79				0.5%	=	Vo	
Vo=0.6V         Vin=12V, lo=60A         76         78           Vo=0.9V         Vin=12V, lo=60A         81         83           Vo=1.2V         Vin=12V, lo=60A         84         86.5           Vo=1.5V         Vin=12V, lo=60A         86         88.5           Vo=1.8W         Vin=12V, lo=60A         88         90.0           Vo=2.5V         Vin=12V, lo=60A         90         92.1           Vo=3.3V         Vin=12V, lo=60A         91         93.4           Vo=5.0V         Vin=12V, lo=60A         92         94.5           SINK EFFICIENCY         Vin=12V, lo=60A         93         FEATURE CHARACTERISTICS           Switching Frequency         Fixed, Per phanse         500         ON/OFF Control           Logic High         Module On (or leave the pin open)         1.3         4.2           Logic Low         Module Off         -0.3         0.7           Remote Sense Range         0.5         0.5           Power Good         Vo is out off +/-10% Vo         0         0.4           Vo is within +/-10% Vo         4.0         5.1           Calculated MTBF         25°C, 300LFM, 80% load         TBD	EEELOUENOV	ESK ≈ 1mU	0		5000	μF	
Vo=0.9V         Vin=12V, Io=60A         81         83           Vo=1.2V         Vin=12V, Io=60A         84         86.5           Vo=1.5V         Vin=12V, Io=60A         86         88.5           Vo=1.8V         Vin=12V, Io=60A         88         90.0           Vo=2.5V         Vin=12V, Io=60A         90         92.1           Vo=3.3V         Vin=12V, Io=60A         91         93.4           Vo=5.0V         Vin=12V, Io=60A         92         94.5           SINK EFFICIENCY         Vo=5.0V         Vin=12V, Io=60A         93           FEATURE CHARACTERISTICS         93         Fixed, Per phanse         500           ON/OFF Control         Positive logic (internally pulled high)         1.3         4.2           Logic High         Module On (or leave the pin open)         1.3         4.2           Logic Low         Module Off         -0.3         0.7           Remote Sense Range         0.5         0.7           Power Good         Vo is out off +/-10% Vo         0         0.4           Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS         TBD         TBD		Vin=12\/ In=60A	70	70		0/	
Vo=1.2V         Vin=12V, lo=60A         84         86.5           Vo=1.5V         Vin=12V, lo=60A         86         88.5           Vo=1.8V         Vin=12V, lo=60A         88         90.0           Vo=2.5V         Vin=12V, lo=60A         90         92.1           Vo=3.3V         Vin=12V, lo=60A         91         93.4           Vo=5.0V         Vin=12V, lo=60A         92         94.5           SINK EFFICIENCY           Vo=5.0V         Vin=12V, lo=60A         93         93           FEATURE CHARACTERISTICS           Switching Frequency         Fixed, Per phanse         500         0           ON/OFF Control         Positive logic (internally pulled high)         1.3         4.2           Logic Low         Module On (or leave the pin open)         1.3         4.2           Logic Low         Module Off         -0.3         0.7           Remote Sense Range         0.5         0.5           Power Good         Vo is within +/-10% Vo         0         0.4           Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS         TBD         TBD						% %	
Vo=1.5V         Vin=12V, lo=60A         86         88.5           Vo=1.8V         Vin=12V, lo=60A         88         90.0           Vo=2.5V         Vin=12V, lo=60A         90         92.1           Vo=3.3V         Vin=12V, lo=60A         91         93.4           Vo=5.0V         Vin=12V, lo=60A         92         94.5           SINK EFFICIENCY           Vo=5.0V         Vin=12V, lo=60A         93         Page 1           FEATURE CHARACTERISTICS           Switching Frequency         Fixed, Per phanse         500         Power Control         Positive logic (internally pulled high)         Power Control         Positive logic (internally pulled high)         Power Control						%	
Vo=2.5V         Vin=12V, lo=60A         90         92.1         90         92.1         93.4         90         92.1         93.4         92         94.5         92         94.5         92         94.5         93         94         93         94         93         94         93         94         94         94         94         94         94         94         94         94         94         94         94         94         94         94         94						%	
Vo=3.3V         Vin=12V, lo=60A         91         93.4         92         94.5         92         94.5         93         94         93         93         93         93         93         93         94         94         94         94         94         94         94         94         94         94         94         94         94         94 <th< td=""><td></td><td colspan="2">Vin=12V, lo=60A</td><td></td><td></td><td>%</td></th<>		Vin=12V, lo=60A				%	
Vo=5.0V         Vin=12V, lo=60A         92         94.5           SINK EFFICIENCY         Vin=12V, lo=60A         93           Vo=5.0V         Vin=12V, lo=60A         93           FEATURE CHARACTERISTICS           Switching Frequency         Fixed, Per phanse         500           ON/OFF Control         Positive logic (internally pulled high)		,				%	
SINK EFFICIENCY           Vo=5.0V         Vin=12V, lo=60A         93           FEATURE CHARACTERISTICS           Switching Frequency         Fixed, Per phanse         500           ON/OFF Control         Positive logic (internally pulled high)         -           Logic High         Module On (or leave the pin open)         1.3         4.2           Logic Low         Module Off         -0.3         0.7           Remote Sense Range         0.5         0.5           Power Good         Vo is out off +/-10% Vo         0         0.4           Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS         TBD         TBD						<b>%</b> %	
Vo=5.0V         Vin=12V, lo=60A         93           FEATURE CHARACTERISTICS           Switching Frequency         Fixed, Per phanse         500           ON/OFF Control         Positive logic (internally pulled high)		VIN=12V, I0=6UA		J4.U		-/0	
FEATURE CHARACTERISTICS           Switching Frequency         Fixed, Per phanse         500           ON/OFF Control         Positive logic (internally pulled high)		Vin=12V, lo=60A		93		%	
Switching Frequency         Fixed, Per phanse         500           ON/OFF Control         Positive logic (internally pulled high)         1.3           Logic High         Module On (or leave the pin open)         1.3         4.2           Logic Low         Module Off         -0.3         0.7           Remote Sense Range         0.5         0.5           Power Good         Vo is out off +/-10% Vo         0         0.4           Vo is within +/-10% Vo         4.0         5.1           Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS         TBD         TBD				30		,0	
Logic High         Module On (or leave the pin open)         1.3         4.2           Logic Low         Module Off         -0.3         0.7           Remote Sense Range         0.5         0.5           Power Good         Vo is out off +/-10% Vo         0         0.4           Vo is within +/-10% Vo         4.0         5.1           Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS         TBD         TBD				500		KHz	
Logic Low         Module Off         -0.3         0.7           Remote Sense Range         0.5         0.5           Power Good         Vo is out off +/-10% Vo         0         0.4           Vo is within +/-10% Vo         4.0         5.1           Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS           Calculated MTBF         25°C, 300LFM, 80% load         TBD		Positive logic (internally pulled high)					
Remote Sense Range         0.5           Power Good         Vo is out off +/-10% Vo         0         0.4           Vo is within +/-10% Vo         4.0         5.1           Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS         Calculated MTBF         25°C, 300LFM, 80% load         TBD						V	
Power Good         Vo is out off +/-10% Vo         0         0.4           Vo is within +/-10% Vo         4.0         5.1           Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS         Calculated MTBF         25°C, 300LFM, 80% load         TBD         TBD		Module Off				V	
Vo is within +/-10% Vo         4.0         5.1           Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS         0.1         2           Calculated MTBF         25°C, 300LFM, 80% load         TBD		Vo is out off +/-10% Vo				V	
Output to Power Good Delay Time         0.1         2           GENERAL SPECIFICATIONS         0.1         2           Calculated MTBF         25°C, 300LFM, 80% load         TBD         TBD	. 551 5000					V	
GENERAL SPECIFICATIONS         25°C, 300LFM, 80% load         TBD	Output to Power Good Delay Time			0.1		ms	
		25°C, 300LFM, 80% load		TBD		Mhours	
Weight     26.5       Over-Temperature Shutdown     Refer to Figure 32 for the measuring point     115	Weight			26.5		grams °C	

#### **ELECTRICAL CHARACTERISTICS CURVES**



**Figure 1:** Converter efficiency vs. output current (0.9V output voltage, 5V&12V input)

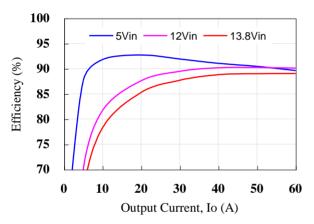


Figure 3: Converter efficiency vs. output current (1.8V output voltage, 5V&12V input)

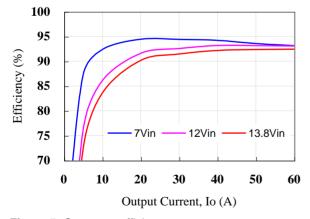


Figure 5: Converter efficiency vs. output current (3.3V output voltage, 12V input)

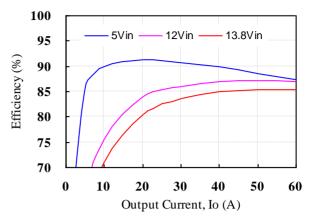


Figure 2: Converter efficiency vs. output current (1.2V output voltage, 5V&12V input)

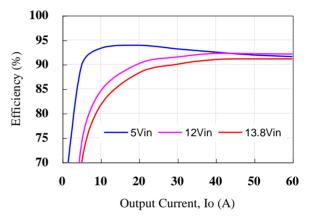
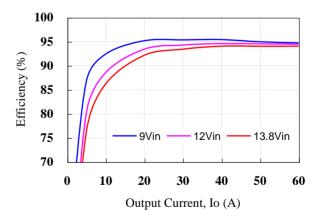


Figure 4: Converter efficiency vs. output current (2.5V output voltage, 5V&12V input)



**Figure 6:** Converter efficiency vs. output current (5.0V output voltage, 12V input)

# **ELECTRICAL CHARACTERISTICS CURVES (CON.)**

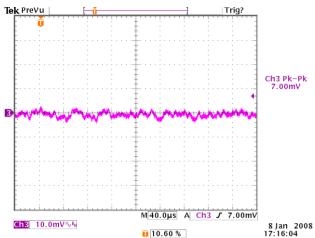


Figure 7: Output ripple & noise at 12Vin, 0.9V/60A out (5mv/div, 1uS/div)

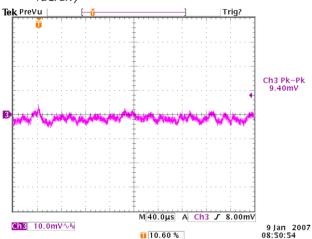


Figure 9: Output ripple & noise at 12Vin, 1.8V/60A out (5mv/div, 1uS/div)

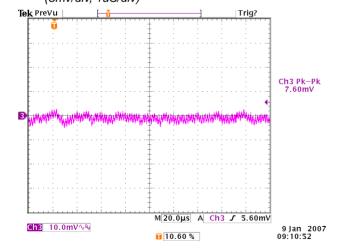
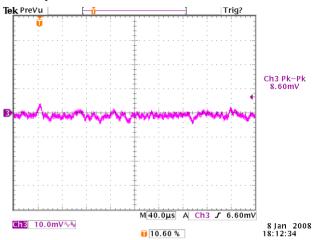


Figure 11: Output ripple & noise at 12Vin, 3.3V/60A out (10mv/div, 1uS/div)



**Figure 8:** Output ripple & noise at 12Vin, 1.2V/60A out (5mv/div, 1uS/div)

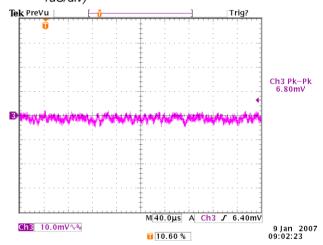


Figure 10: Output ripple & noise at 12Vin, 2.5V/60A out (5mv/div, 1uS/div)

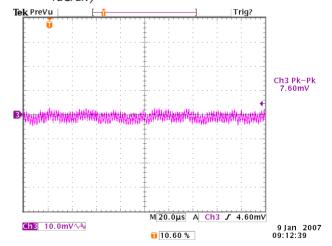


Figure 12: Output ripple & noise at 12Vin, 5.0V/60A out (10mv/div, 1uS/div)

# ELECTRICAL CHARACTERISTICS CURVES (CON.) 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 10:03:54 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012/10/19 | 2012

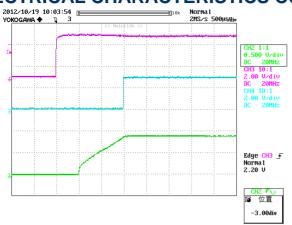


Figure 13: Turn on delay time at 12Vin, 0.9V/60A out (500uS/div) Ch2: Vo, Ch3: Enable, Ch4:PG

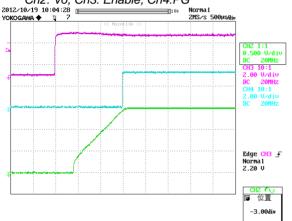


Figure 15: Turn on delay time at 12Vin, 1.5V/60A out (500uS/div) Ch2: Vo, Ch3: Enable, Ch4:PG

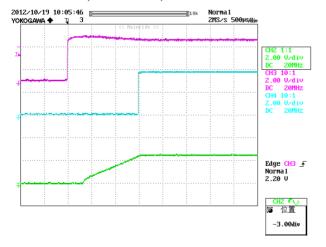


Figure 17: Turn on delay time at 12Vin, 2.5V/60A out (500uS/div) Ch2: Vo, Ch3: Enable, Ch4:PG

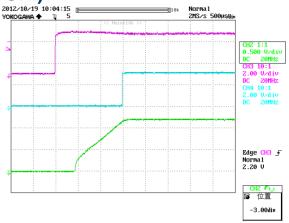


Figure 14: Turn on delay time at 12Vin, 1.2V/60A out (500uS/div) Ch2: Vo, Ch3: Enable, Ch4:PG

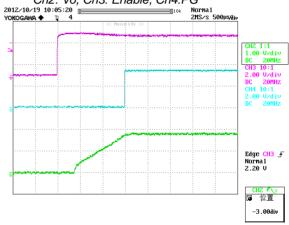


Figure 16: Turn on delay time at 12Vin, 1.8V/60A out (500uS/div) Ch2: Vo, Ch3: Enable, Ch4:PG

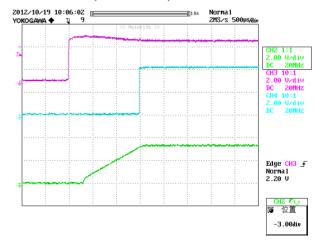


Figure 18: Turn on delay time at 12Vin, 3.3V/60A out (500uS/div) Ch2: Vo, Ch3: Enable, Ch4:PG

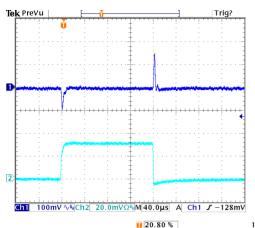


Figure 19: Typical transient response to step load change at 10A/μS from 50%to 100% and 100% to 50 of lo, max at 12Vin, 0.9V out (0.100V/div)

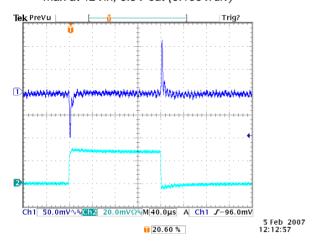


Figure 21: Typical transient response to step load change at 10A/μS from 50%to 100% and 100% to 50 of lo, max at 12Vin, 1.8V out (0.100V/div)

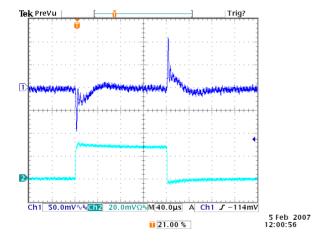


Figure 23: Typical transient response to step load change at 10A/μS from 50%to 100% and 100% to 50 of Io, max at 12Vin, 3.3V out (0.100V/div)

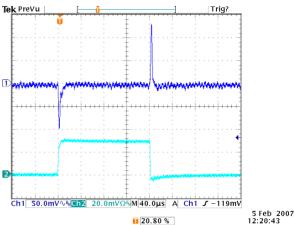


Figure 20: Typical transient response to step load change at 10A/μS from 50%to 100% and 100% to 50 of lo, max at 12Vin, 1.2V out (0.100V/div)

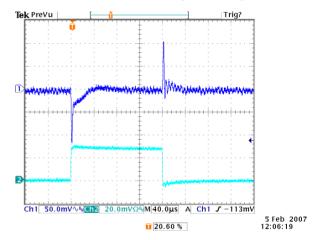


Figure 22: Typical transient response to step load change at 10A/μS from 50%to 100% and 100% to 50 of lo, max at 12Vin, 2.5V out (0.100V/div)

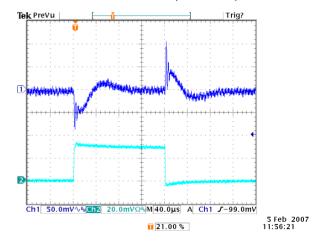


Figure 24: Typical transient response to step load change at 10A/μS from 50%to 100% and 100% to 50 of lo, max at 12Vin, 5.0V out (0.100V/div)

#### **DESIGN CONSIDERATIONS**

The D12S300-1 uses a three phase and voltage mode controlled buck topology. The output can be trimmed in the range of 0.6Vdc to 5Vdc by a resistor from Trim pin to Ground.

The converter can be turned ON/OFF by remote control. Positive on/off (ENABLE pin) logic implies that the converter DC output is enabled when the signal is driven high (greater than 1.2V) or floating and disabled when the signal is driven low (below 0.7V). Negative on/off logic is optional.

The converter provides an open collector Power Good signal. The power good signal is pulled low when output is not within ±10% of Vout or Enable is OFF.

The converter can protect itself by entering hiccup mode against over current and short circuit condition.

#### **Safety Considerations**

It is recommended that the user to provide a fuse in the input line for safety. The output voltage set-point and the output current in the application could define the amperage rating of the fuse.

#### FEATURES DESCRIPTIONS

#### Enable (On/Off)

The ENABLE (on/off) input allows external circuitry to put the D12S300-1 converter into a low power dissipation (sleep) mode. Positive ENABLE is available as standard.

Positive ENABLE units of the D12S300-1 series are turned on if the ENABLE pin is high or floating. Pulling the pin low will turn off the unit. With the active high function, the output is guaranteed to turn on if the ENABLE pin is driven above 1.3V. The output will turn off if the ENABLE pin voltage is pulled below 0.7V.

The ENABLE input can be driven in a variety of ways as shown in Figures 25 and 26. If the ENABLE signal comes from the primary side of the circuit, the ENABLE can be driven through either a bipolar signal transistor (Figure 25). If the enable signal comes from the secondary side, then an opto-coupler or other isolation devices must be used to bring the signal across the voltage isolation (please see Figure 26).

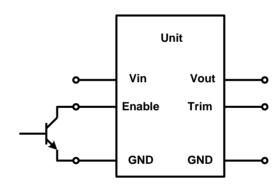


Figure 25: Enable Input drive circuit for D12S300-1 series

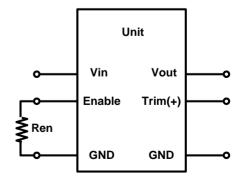


Figure 26: Enable input drive circuit example with isolation.

### FEATURES DESCRIPTIONS (CON.)

#### Input Under-Voltage Lockout

The input under-voltage lockout prevents the converter from being damaged while operating when the input voltage is too low. The lockout occurs between 4.1V to 4.5V.

#### Over-Current and Short-Circuit Protection

The D12S300-1 series modules have non-latching over-current and short-circuit protection circuitry. When over current condition occurs, the module goes into the non-latching hiccup mode. When the over-current condition is removed, the module will resume normal operation.

An over current condition is detected by measuring the voltage drop across the inductor. The voltage drop across the inductor is also a function of the inductor's DCR.

Note that none of the module specifications are guaranteed when the unit is operated in an over-current condition.

#### **Remote Sense**

The D12S300-1 provides Vo remote sensing to achieve proper regulation at the load points and reduce effects of distribution losses on output line. In the event of an open remote sense line, the module shall maintain local sense regulation through an internal resistor. The module shall correct for a total of 0.6V of loss. The remote sense connects as shown in Figures 27.

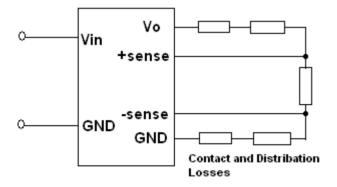


Figure 27: Circuit configuration for remote sense

#### **Output Voltage Programming**

The output voltage of the NE series is trimmable by connecting an external resistor between the trim pin and output ground as shown Figure 28 and the typical trim resistor values are shown in Table 1.

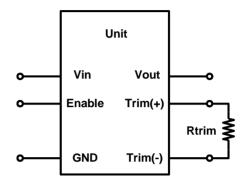


Figure 28: Trimming Output Voltage

The D12S300-1 module has a trim range of 0.6V to 5V. The trim resistor equation for the D12S300-1 is:

$$Rs(\Omega) = \frac{1200}{Vout - 0.6}$$

Vout is the output voltage setpoint Rs is the resistance between Trim and Ground Rs values should not be less than  $270\Omega$ 

Output Voltage	Rs (Ω)
0.6V	open
+0.9V	4K
+1.2V	2K
+1.5 V	1.33K
+1.8V	1K
+2.5 V	631.6
+3.3 V	444.4
+5.0V	272.7

Table 1: Typical trim resistor values

#### **Power Good**

The converter provides an open collector signal called Power Good. This output pin uses positive logic and is open collector. This power good output is able to sink 5mA and set high when the output is within  $\pm 10\%$  of output set point. The power good signal is pulled low when output is not within  $\pm 10\%$  of Vout or Enable is OFF.

# FEATURES DESCRIPTIONS (CON.)

#### **Current Sharing (optional)**

The parallel operation of multiple converters is available with the D12S300-1 E. The converters will current share to be within +/- 10% of each other. In additional to connect the I-Share pin together for the current sharing operation, the remote sense lines of the paralleled units must be connected at the same point for proper operation. Also, units should be turned on/off by enable at the same time. Hot plugging is not recommended. The current sharing diagram show in figure 29.

# D12S300-1 E Vout Cout +sense -sense GND Enable I-share Trim Load Vout Cout +sense -sense GND Enable I-share Trim

Figure 29: Current sharing diagram

#### **Voltage Margining Adjustment**

Output voltage margin adjusting can be implemented in the ND modules by connecting a resistor, Rmargin-up, from the Trim pin to the Ground for margining up the output voltage. Also, the output voltage can be adjusted lower by connecting a resistor, Rmargin-down, from the Trim pin to the voltage source Vt. Figure 30 shows the circuit configuration for output voltage margining adjustment.

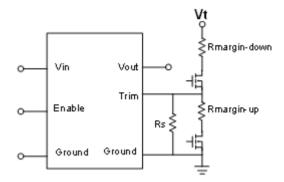


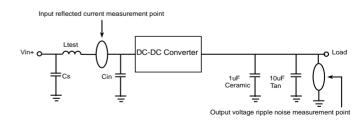
Figure 30: Circuit configuration for output voltage margining

#### **Output Capacitance**

There are internal output capacitors on the D12S300-1 series modules. Hence, no external output capacitor is required for stable operation.

# Reflected Ripple Current and Output Ripple and Noise Measurement

The measurement set-up outlined in Figure 31 has been used for both input reflected/ terminal ripple current and output voltage ripple and noise measurements on D12S300-1 series converters.



Cs=330 $\mu$ F OS-CON cap x 1, Ltest=1 $\mu$ H, Cin=330 $\mu$ F OS-CON cap x 1

Figure 31: Input reflected ripple/ capacitor ripple current and output voltage ripple and noise measurement setup for D12S300-1

#### THERMAL CONSIDERATION

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

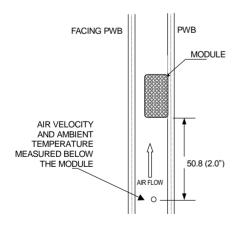
#### **Thermal Testing Setup**

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module is constantly kept at 6.35mm (0.25").

#### **Thermal Derating**

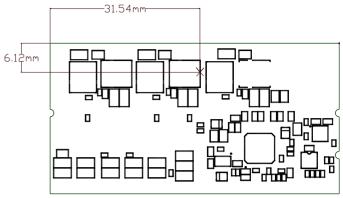
Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inches)

Figure 32: Wind tunnel test setup

# THERMAL CURVES (D12S300-1)



**Figure 33:** Temperature measurement location\* The allowed maximum hot spot temperature is defined at 115  $^{\circ}$ 

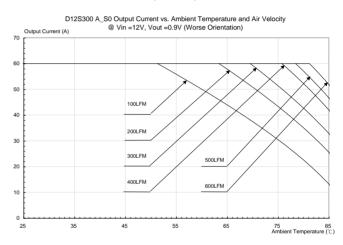


Figure 34: Output current vs. ambient temperature and air velocity @Vin=12V, Vout=0.9V (Worse Orientation)

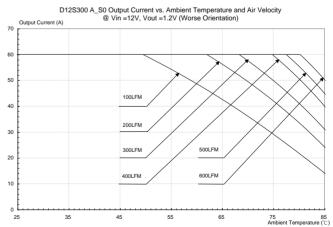


Figure 35: Output current vs. ambient temperature and air velocity @ Vin=12V, Vout=1.2V (Worse Orientation)

## THERMAL CURVES (D12S300-1)

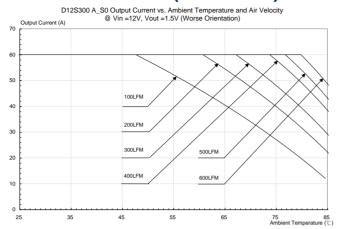


Figure 36: Output current vs. ambient temperature and air velocity @ Vin=12V, Vout=1.5V (Worse Orientation)

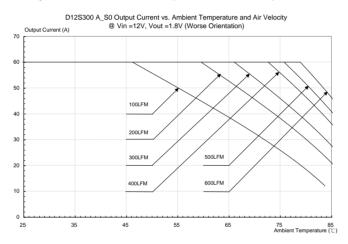


Figure 37: Output current vs. ambient temperature and air velocity @Vin=12V, Vout=1.8V (Worse Orientation)

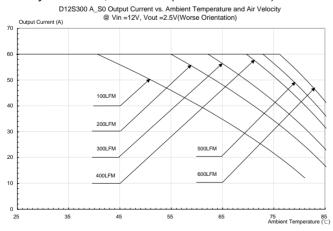


Figure 38: Output current vs. ambient temperature and air velocity @Vin=12V, Vout=2.5V (Worse Orientation)

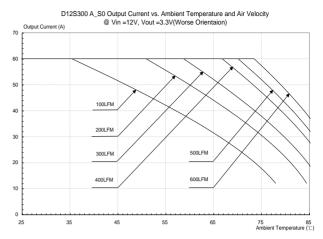
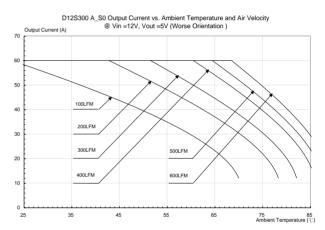


Figure 39: Output current vs. ambient temperature and air velocity @ Vin=12V, Vout=3.3V (Worse Orientation)

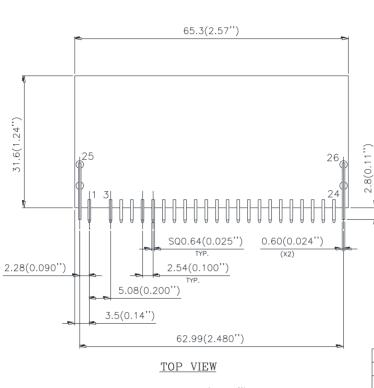


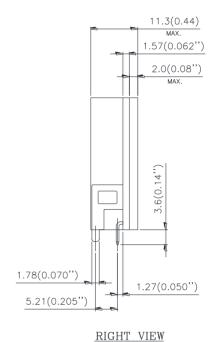
**Figure 40:** Output current vs. ambient temperature and air velocity @ Vin=12V, Vout=5V (Worse Orientation)

DS\_D12S300-1\_07212016

#### **MECHANICAL DRAWING**

#### **VERTICAL**





PIN#	Function	PIN#	Function		
1	TRIM +	14	Vin		
2	OMIT (KEY)	15	Vout		
3	GROUND	16	Vout		
4	POWER GOOD	17	GROUND		
5	TRIM -	18	Vout		
6	ISHARE	19	GROUND		
7	GROUND	20	Vout		
8	GROUND	21	GROUND		
9	ENABLE	22	Vout		
10	REM SENSE (-)	23	GROUND		
11	REM SENSE (+)	24	Vout		
12	Vin	25	MECH SUPPORT		
13	Vin	26	MECH SUPPORT		

RECOMMENDED P.W.B LAYOUT

NOTES:
DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)

X.XXmm±0.25mm(X.XXX in.±0.010 in.)

#### PART NUMBERING SYSTEM

D	12	S	300	-1 E
Type of Product	Input Voltage	Number of Outputs	<b>Product Series</b>	Option Code
D - DC/DC modules	4.5 - 12 -13.8V	S - Single Output	300 - 60A	D- without current sharing     E- current sharing

#### **MODEL LIST**

Model Name	Packaging	Input Voltage	Output Voltage	Output Current	Efficiency 12Vin, Max Vout @ 100% load
D12S300-1 D	Vertical	4.5 ~ 13.8Vdc	0.6 V~5.0Vdc	60A	94%
D12S300-1 E	Vertical	4.5 ~ 13.8Vdc	0.6 V~3.3Vdc	60A	92%

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#### **WARRANTY**

Delta offers a two (2) year limited warranty. Complete warranty information is listed on our web site or is available upon request from Delta.

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