BPC2425M7X60

Power LDMOS module

Rev. 1 — 29 March 2018

1. Product profile

1.1 General description

60 W LDMOS power module with excellent gain flatness for Industrial, Scientific and Medical (ISM) applications at frequencies from 2400 MHz to 2500 MHz. The module is designed as a dual stage high gain medium power amplifier for CW and pulsed applications.

Table 1. Test information

Typical RF performance at $V_{DS} = 32$ V; $T_{mb} = 25$ °C; $I_{Dq1(A)} = I_{Dq1(B)} = 25$ mA; $I_{Dq2(A)} = I_{Dq2(B)} = 50$ mA.

| Test signal | f | V _{DS} | PL | G _p | ησ |
|---------------|-------|-----------------|-----|----------------|-----|
| | (MHz) | (V) | (W) | (dB) | (%) |
| CW | 2450 | 32 | 60 | 26 | 41 |
| CW pulsed [1] | 2450 | 32 | 60 | 26.5 | 42 |

[1] Pulse width is 300 μ s; duty cycle is 50 %.

1.2 Features and benefits

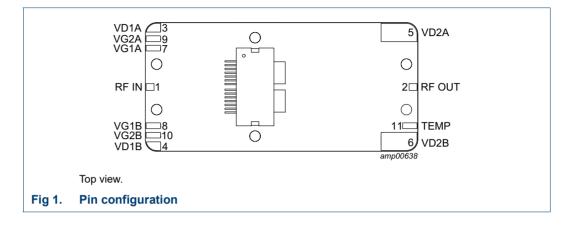
- Flat gain
- Small size: 72 × 34 mm
- Input/output 50 Ω matched
- Balanced configuration
- Designed for broadband operation (2400 MHz to 2500 MHZ)
- Built-in temperature sensor
- Built-in temperature compensation in biasing networks
- 100 % RF testing in production
- For RoHS compliance see the product details on the Ampleon website

1.3 Applications

 RF power amplifiers for CW applications in the 2400 MHz to 2500 MHz frequency range such as industrial heating and drying, scientific, medical, plasma lighting and solid state cooking

2. Pinning information

2.1 Pinning



2.2 Pin description

Table 2. Pin description

| Symbol | Pin | Description |
|--------|-----|--|
| RF IN | 1 | RF input |
| RF OUT | 2 | RF output |
| VD1A | 3 | drain-source voltage driver, section A |
| VD1B | 4 | drain-source voltage driver, section B |
| VD2A | 5 | drain-source voltage final, section A |
| VD2B | 6 | drain-source voltage final, section B |
| VG1A | 7 | gate-source voltage driver, section A |
| VG1B | 8 | gate-source voltage driver, section B |
| VG2A | 9 | gate-source voltage final, section A |
| VG2B | 10 | gate-source voltage final, section B |
| TEMP | 11 | temperature sensor |

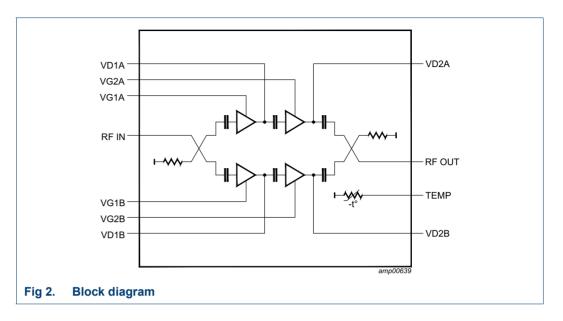
3. Ordering information

Table 3.Ordering information

| Type number | Packag | Package | | | |
|--------------|--------|---|---------|--|--|
| | Name | Description | Version | | |
| BPC2425M7X60 | - | pallet LDMOS; 6 mounting holes; 11 terminations | - | | |

Power LDMOS module

4. Block diagram



5. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|---------------------------|---------------|-----|-----|------|
| V _{DS} | drain-source voltage | non operating | 0 | 65 | V |
| V _{GS} | gate-source voltage | non operating | -6 | +13 | V |
| T _{stg} | storage temperature | | -65 | +85 | °C |
| T _{mb} | mounting base temperature | | 0 | 85 | °C |

6. Characteristics

Table 5. DC characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------------|--------------------------------|--|-----|------|------|------|
| V _{(BR)DSS} | drain-source breakdown voltage | V _{GS} = 0 V; I _D = 2.7 mA | 65 | - | - | V |
| V _{GS(th)} | gate-source threshold voltage | driver (VG1A, VG1B): V _{DS} = 32 V; I _D = 25 mA | - | 1.95 | - | V |
| | | final (VG2A, VG2B); V _{DS} = 32 V; I _D = 50 mA | - | 1.85 | - | V |
| I _{DSS} | drain leakage current | V _{GS} = 0 V; V _{DS} = 32 V | - | - | 4.20 | μA |
| R _{GS} | gate-source resistance | | 300 | 1500 | 5000 | Ω |
| C _{iss} | input capacitance | VG1A, VG2B pins | - | 0.01 | - | μF |
| | | VD1A, VD2B pins | - | 0.47 | - | μF |

Table 6. RF Characteristics

Test signal: CW; RF performance at T_{mb} = 25 °C; V_{DS} = 32 V; $I_{Dq1(A)} = I_{Dq1(B)}$ = 25 mA; $I_{Dq2(A)} = I_{Dq2(B)}$ = 50 mA; unless otherwise specified; in a class-AB production test circuit.

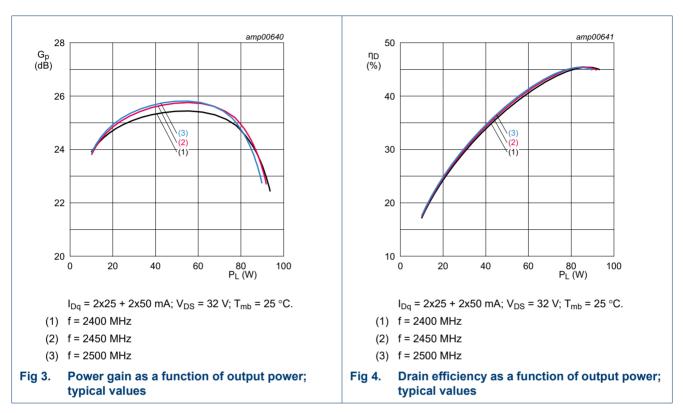
| Symbol | Parameter | Conditions | Min | Тур | Мах | Unit |
|---------------------|---------------------------------------|--|------|-----|------|------|
| G _p | power gain | P _L = 60 W; f = 2400 MHz to f = 2500 MHz | 25 | 26 | - | dB |
| P _{L(1dB)} | output power at 1 dB gain compression | f = 2400 MHz to f = 2500 MHz | - | 80 | - | W |
| P _{L(3dB)} | output power at 3 dB gain compression | f = 2400 MHz to f = 2500 MHz | - | 90 | - | W |
| f | frequency | P _L = 60 W | 2400 | - | 2500 | MHz |
| G _{flat} | gain flatness | P _L = 60 W; f = 2400 MHz to f = 2500 MHz | - | 0.5 | - | dB |
| RL _{in} | input return loss | P _L = 60 W; f = 2400 MHz to f = 2500 MHz | - | -25 | -12 | dB |
| η _D | drain efficiency | P _L = 60 W; f = 2450 MHz | 38.5 | 41 | - | % |
| $\alpha_{sup(H)}$ | harmonic suppression | P _L = 300 W; f = 2450 MHz | - | 30 | - | dBc |

6.1 Ruggedness in class-AB operation

The BPC2425M7X60 is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases with a time rate of 15 ms/degree under the following conditions: $V_{DS} = 32$ V; $I_{Dq1(A)} = I_{Dq1(B)} = 25$ mA; $I_{Dq2(A)} = I_{Dq2(B)} = 50$ mA; $P_L = 60$ W (CW); f = 2450 MHz; $T_{mb} = 25$ °C.

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7. Test information



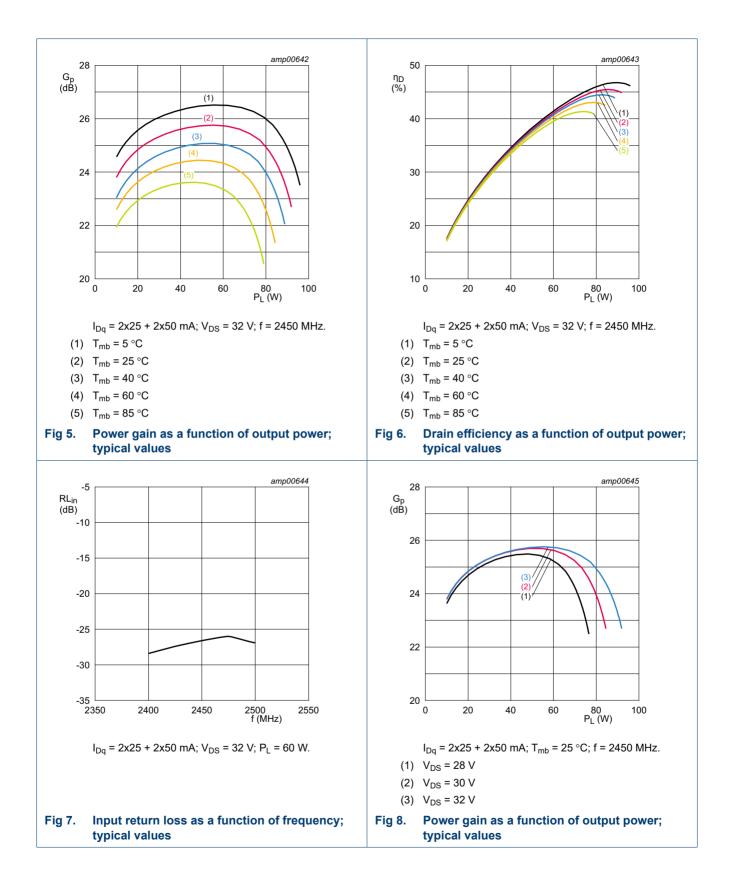
7.1 Graphical data

7.1.1 CW

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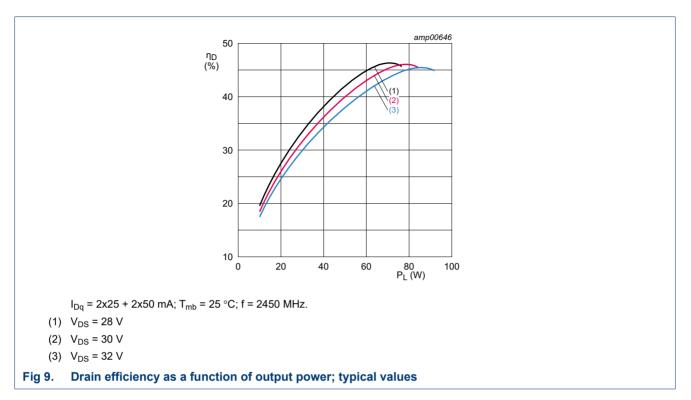


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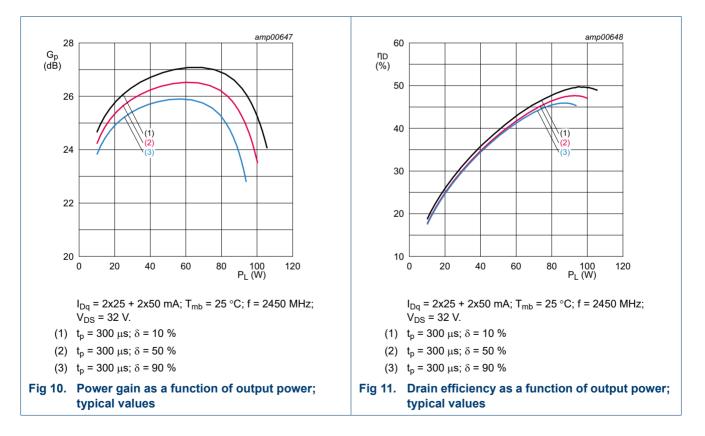
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Power LDMOS module

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7.1.2 CW pulsed



BPC2425M7X60 Power LDMOS module

8. Package outline

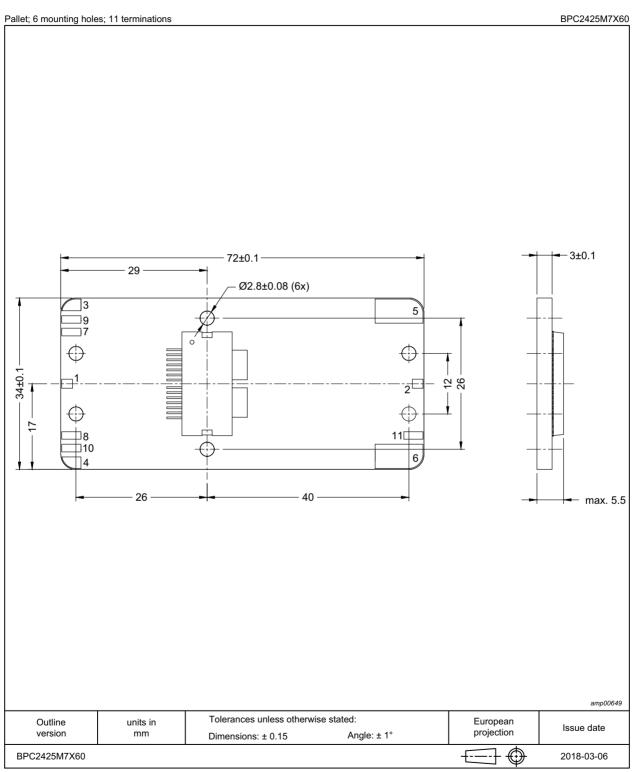


Fig 12. Package outline

BPC2425M7X60

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

Table 7.ESD sensitivity

| ESD model | Class |
|--|-------|
| Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002 | C1 🗓 |
| Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001 | 1C 🛛 |

[1] CDM classification C1 is granted to any part that passes after exposure to an ESD pulse of 250 V, but fails after exposure to an ESD pulse of 500 V.

[2] HBM classification 1C is granted to any part that passes after exposure to an ESD pulse of 1000 V, but fails after exposure to an ESD pulse of 2000 V.

10. Abbreviations

| Table 8. Abbreviations | | | | |
|------------------------|--|--|--|--|
| Acronym | Description | | | |
| CW | Continuous Wave | | | |
| LDMOS | Laterally Diffused Metal-Oxide Semiconductor | | | |
| MTF | Median Time to Failure | | | |
| RoHS | Restriction of Hazardous Substances | | | |
| VSWR | Voltage Standing Wave Ratio | | | |

11. Revision history

Table 9.Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|------------------|--------------|--------------------|---------------|------------|
| BPC2425M7X60 v.1 | 20180329 | Product data sheet | - | - |

12. Legal information

12.1 Data sheet status

| Document status ^{[1][2]} | Product status ^[3] | Definition |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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